Controls’ Aspects for LHC Hardware Commissioning:

Power Converters

Minutes of Meeting on 30th July, 2003

Participants: Quentin King, Axel Daneels, Hugues Thiesen, Pierre Charrue, Robin Lauckner

Commissioning Phases

1. 1 April 2004 – 30 June 2004:
   Individual reception tests of FGC together with Voltage Source + DCCT on test bench in Bd 866-1-C16.
   A first batch of 200 FGC should be delivered by Easter 2004 (12/04/2004), followed by 1600 more FGC in batches of 200 at the rate of one batch every 6 weeks. Test takes ~ 4 weeks / batch.
   Requests:
   - LabVIEW
   - timing (at least the Controls Timing Generator card CTG)
   - DB (calibration)
   - 8 Gateways systems
   - 8 WorldFIP
   Note: no piquet service requested outside working hours for this test bench

2. 1 July – 30 Sept 2004:
   Integration Tests in Bd. 377 & 287

3. 1 Oct 2004- 28 Feb 2005:
   Installation & Test in situ.
   Requests:
   - Gateways installed
   - WorldFIP installed
   - ...

4. 1 March 2005:
   HW Commissioning.
   Requests:
   - RT
   - Alarms
   - Logging
   - Post-Mortem
   - Interlocks
   - Application SW (Hatziangeli)
   - Etc.

Major Milestone for AB/CO:

1. Choice of Gateway Hardware end Sept 2003 – Marc VDE
2. Reference Gateway and Reference CTG in CO lab, end Jan 2004 – Wolfgang Heinz
3. 8 Gateway Systems, LHC timing signals to PO, end Mar 2004 – Wolfgang Heinz
4. Specification of Post Mortem Interface to PO, end June 2004 – Robin Lauckner
7. Machine Interlocks connected to PO, March 2005 – Bruno Puccio

Other items
- Integration of PO equipment into Control System Configuration database – Ronny Billen
- Availability in Control System of PO calibration data from Burn-In tests – Ronny Billen
- Define RT project – Robin Lauckner

P. Charrue's comment:
Could AB/PO express their needs for AB/CO support and piquet service during their tests in Bd.866 and 377 as well as for the in-situ installation and tests?