

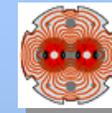


EXPERIENCE FROM STRING II: QUENCH PROTECTION SYSTEM

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Controlled QPS Equipment in String II

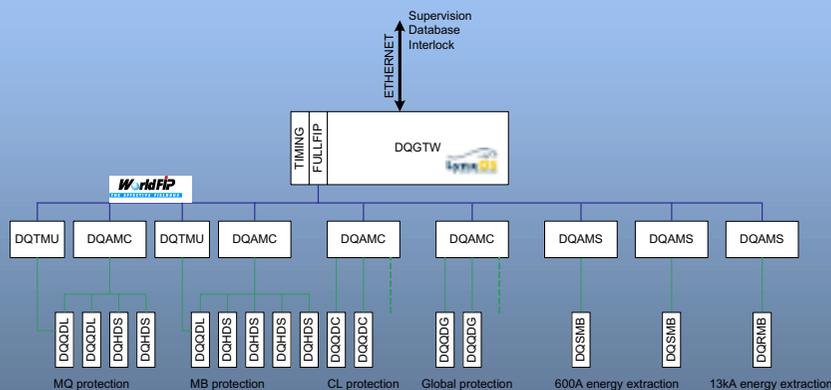
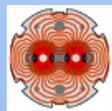


- u **QPS-supervision**
 - n Quench detectors
 - n Quench heater power supplies
 - n Test mode units
 - n Switch controllers
- u **QPS-experiments**
 - n Signal conditioners (gain setting directly via STRICT)
 - n Test mode units (provoked quenches)

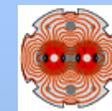
Equipment	Description	Phase I	Phase II
DQQDL	Local quench detector	7	10
DQHDS	Quench heater power supply	20	32
DQTMU	Test mode unit	7	10
DQQDC	Protection system for HTS leads	16	16
DQQDG	Global quench detector	15	15
DQAMC	Acquisition & monitoring controller	17	20
DQAMS	Acquisition & monitoring controller for switch controllers	3	3



QPS String II Communication



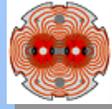
Fieldbuses



- u **Fieldbus I**
 - n 27(33) clients
 - n MicroFip in stand-alone (DQTMU) and micro-controlled mode (DQAMC, DQAMS)
 - n Master is PowerPC running LynxOs
- u **Fieldbus II**
 - n 17(20) clients
 - n MicroFip in stand-alone mode
 - n Master is ordinary PC running some Microsoft LynxOs
- u **Fieldbus general parameters**
 - n 1 Mbit/s
 - n Only periodic traffic
 - n Macro-cycle length of 100ms
 - n Timing synchronization via fieldbus



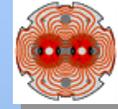
Participation



- u **LHC-ICP**
 - n QPS hardware
 - n Programming DQAMC (acquisition & monitoring controllers)
 - n Definition of signals
 - n Partial programming of MPGATE application
- u **LHC-IAS**
 - n STRIDE, STRICT, generic DAQ & interfaces
 - n Supervision application
 - n Support for fieldbus, local timing and DQAMC
- u **SL-CO**
 - n Hardware for MP-gateway & servers
 - n Programming of MPGATE application including OPC-server for supervision application
 - n GPS-timing
 - n Interlocks



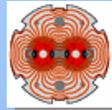
Commissioning I



- u **Access to equipment in the field necessary**
- u **Fieldbus**
 - n No installation faults
 - n Some problems with connectors & daisy-chaining inside of QPS racks
 - n Easy to be checked as bus is easy accessible in String II
 - n Straps required for commissioning & phase I
 - n Short commissioning time also due to full availability of MP-gateway
- u **DQAMC & DQAMS**
 - n Local serial ports very useful for commissioning
 - n Timing synchronization



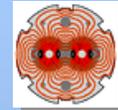
Commissioning II



- u **Test mode units**
 - n Access not inhibited in case of powered circuits
- u **MPGATE application**
 - n Stable version ready and tested in time
 - n Smoothly running so far
 - n GPS-timing implemented (problems with precision)
- u **MPGATE interfaces**
 - n Supervision
 - 1 Only minor problems (e.g. missing variables)
 - n Historian (STRIDE)
 - 1 Interface not working at begin of String II commissioning
 - 1 Still not fully functional (i.e. alarms, units)
 - n Interlock (Power-Permit transferred via supervision)
 - 1 Permit test option had to be added during commissioning



Problems I: "Cohabitation"



- | | |
|---|--|
| <ul style="list-style-type: none"> u Equipment <ul style="list-style-type: none"> n Equipment & people met each other prior to installation n But real equipment didn't fit into the foreseen rack | <ul style="list-style-type: none"> u Signals <ul style="list-style-type: none"> n Current lead temperature to be shared with cryogenics n Sensor signal conditioned by QPS and routed back to cryogenics n QPS speaks [V], cryogenics speaks [4..20mA] n Problems with maintenance (i.e. exchange of a quench detector) |
|---|--|
- Nevertheless:
things are working properly !**



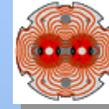
Problems II: Physical Units



- u Analog values intrinsically coded into “signed integer” format (2 bytes)
- u Conversion factors to be shipped to:
 - n Supervision
 - n STRIDE
 - n Generic DAQ (including additional STRICT settings)
- u Several lists to be synchronized
- u Data in physical units not fully available in phase I – hopefully in phase II



LHC



- u WorldFip 1Mbit/s
 - n 4 busses per sector for equipment in tunnel (50 clients)
 - n 1 dedicated bus per sector for 13kA busbar protection
 - n 1 local bus per UA, RR, UJ ...
- u Revised design for DQAMC / DQAMS
 - n Design reduced to required functionality (single chip DAQ)
 - n Test mode unit integrated
 - n Radiation tolerant
- u Reduced number of signals per equipment
 - n Optimised number of DQAMC & DQAMS